

일시 : 2018년 8월 29일(수)~30일(목) | 장소 : 경기도 분당 'CST 한국지사'

교육 프로그램

| 시간 | 교육내용 |
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| 제 1 일 (2018년 8월 29일, 수요일) - CST 한국지사 Application Engineer | |
| 09:30~10:30 | <ul style="list-style-type: none"> □ Introduction of CST products and Applications |
| 10:30~12:00 | <ul style="list-style-type: none"> □ Handling of CST PCB STUDIO® GUI □ Fast and Efficient Verifying the Layout Design for PCB Structure Using CST BOARDCHECK™ <ul style="list-style-type: none"> - Automatic Report and Display of Certain Violation • General Workflow <ul style="list-style-type: none"> - Importing Various PCB Layout Format - Define Stack-up, Net-Class (Single, Differential, Power, GND), Component (RLC and IBIS) • SI Rule Check - Net Integrity, Via Integrity, Power Integrity • EMC Rule Check - Signal Reference, Wiring/Crosstalk Decoupling and Stitching Capacitor Placement |
| 12:00~13:00 | <ul style="list-style-type: none"> □ Lunch |
| 13:00~14:30 | <ul style="list-style-type: none"> □ Fast 2D Signal Integrity Analysis Using SI-TD and SI-FD Solver of CST PCB STUDIO® <ul style="list-style-type: none"> - Single-Ended, Differential Pair, SPICE Model and Net List Extraction • SI-FD Analysis - S-Parameter, Cross-Talk • SI-TD Analysis - Transient/Timing Analysis, Signal Delay, EYE Diagram, Cross-Talk |
| 14:30~16:00 | <ul style="list-style-type: none"> □ Fast 2D Power Integrity Analysis using IR-drop and PI solver of CST PCB STUDIO® <ul style="list-style-type: none"> - Power Delivery Network (PDN) Extraction, DC Power Integrity, AC Power Integrity, Transient Power Integrity, Decoupling Capacitor Placement and Property • IR-Drop Analysis - Voltage Drop at Specific Pin, Spatial Current Density and Voltage Drop Plot • HF PI Solver - Impedance Profile for Target Impedance Analysis, Spatial Impedance Plot |
| 16:00~17:00 | <ul style="list-style-type: none"> □ Decoupling Capacitor Analysis <ul style="list-style-type: none"> - Decoupling Capacitor Optimization using De-cap Tool □ Full 3D EMC/EMI Analysis using Transient Solver of CST MICROWAVE STUDIO® <ul style="list-style-type: none"> - Convert 2D PCB Layout to 3D Structure for EMI/C Analysis - Evaluation of EMI/EMC for Common Mode Noise from Differential Signaling - Common Mode Noise Filtering Using CM Filter - 3D Field Distribution (Electric and Magnetic Field) - Calculation of EMC Value at 3m and 10m |

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| 제 2 일 (2018 년 8 월 30 일, 목요일) - CST 한국지사 Application Engineer | |
| 09:30~10:30 | <ul style="list-style-type: none"> □ PCB Import to 3D Model from 2D Data <ul style="list-style-type: none"> - EDA Import : Stack Up, Component and Parts Import, Customize Selection, Automatic Port Definition or Manual Ports Definition in 2D Layout Viewer, Automatic Mesh Settings for Hexahedral Mesh |
| 10:30~12:00 | <ul style="list-style-type: none"> □ Full 3D EMC Analysis <ul style="list-style-type: none"> • Modeling, Simulation Setting • Result Overview <ul style="list-style-type: none"> - EMC Norm, Probe Results (E-Field, H-Field, RCS) |
| 12:00~13:00 | <ul style="list-style-type: none"> □ Lunch |
| 13:00~14:00 | <ul style="list-style-type: none"> □ Radiated Emission Simulation in CST DESIGN STUDIO™ <ul style="list-style-type: none"> • Modeling <ul style="list-style-type: none"> - Block Overview, Data Import(Touchstone, Spice, IBIS, etc.), Task Setting (S-Parameter, Transient, AC, Combine Results, Spectrum Line, Mixer, Amplifier) • Result Overview <ul style="list-style-type: none"> - Port Signal, S-Parameter, Voltage and Current in Time and Frequency Domain, Field Results • Post Processing <ul style="list-style-type: none"> - Radiated Spectrum |
| 14:00~15:30 | <ul style="list-style-type: none"> □ Full 3D ESD Analysis with 3D ESD Gun Model & 3D PCB Data <ul style="list-style-type: none"> • Modeling, Simulation Setting <ul style="list-style-type: none"> - Import the ESD Gun Model - Ideal Current and Voltage Source • IEC 61000-4-2 Standard ESD Gun Model Simulation with Metallic Wall • Result Overview <ul style="list-style-type: none"> - Surface Current, E/H-Field Result |
| 15:30~17:00 | <ul style="list-style-type: none"> □ Shielding Effectiveness Simulation <ul style="list-style-type: none"> • Panel Shielding Effectiveness <ul style="list-style-type: none"> - Define Slot, Vent, Wire - TLM Solver Setting - Calculation of Shielding Effectiveness Using SAM (System Assembly and Modeling) • Full 3D Shielding Effectiveness <ul style="list-style-type: none"> - Calculation of Shielding Effectiveness (Inside Stimulate, Outside Stimulate) |

상기일정은 변경될 수 있습니다

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